5

ABSTRACT OF THE DISCLOSURE

An equalizer includes n processing paths arranged to process blocks of data. N-1 data shifters are arranged so that each of the n-1 data shifters is in a corresponding one of the n processing paths and so that one of the n processing path has no data shifter. N finite filters are arranged so that each of the n finite filters is in a corresponding one of the n processing paths, and so that each of the n finite filters applies a corresponding set of finite filter coefficients to the blocks of data. Ghosts of the blocks of data are not eliminated as a result of the application of the sets of finite filter coefficients corresponding to the n finite filters, and n>2. An adder is arranged to add outputs from the n processing paths such that the addition eliminates ghosts of the blocks of data.